	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
1	IS&R	L4	11	(("5361373") or ("5537601") or ("5652904") or ("5671355") or ("5752035") or ("5970254") or ("6020755") or ("6096091") or ("6279045") or ("6282627") or ("6343207")).PN.	USPAT; US-PGPUB	2003/06/30 13:56	
2	BRS	L5	214	"timing analysis" same "integrated circuit"	USPAT; US-PGPUB	2003/06/30 13:58	
3	BRS	L6	217	timing adj2 analysis same "integrated circuit"	USPAT; US-PGPUB	2003/06/30 13:58	
4	BRS	L7	41	timing adj2 analysis same "integrated circuit" same device	USPAT; US-PGPUB	2003/06/30 13:59	
5	BRS	L8	22	timing adj2 analysis same "integrated circuit" same device same delay	USPAT; US-PGPUB	2003/06/30 13:59	
6	BRS	L9	0	timing adj2 analysis same "integrated circuit" same device same delay same ("clock-to-output" or "clock to output")	USPAT; US-PGPUB	2003/06/30 14:00	
7	BRS	L10	0	timing adj2 analysis same "integrated circuit" same device same ("clock-to-output" or "clock to output")	USPAT; US-PGPUB	2003/06/30 14:00	
8	BRS	L11	0	timing adj2 analysis same "integrated circuit" same ("clock-to-output" or "clock to output")	USPAT; US-PGPUB	2003/06/30 14:00	
9	BRS	L12	4	"integrated circuit" same ("clock-to-output" or "clock to output")	USPAT; US-PGPUB	2003/06/30 14:00	

Inventor search → 0 hit.
burnley - richard, in..

	Type	Hits	Search Text	DBs	Time Stamp	Comments
10	BRS	4	pcb same "thermal analysis"	USPAT; US-PGPUB	2003/06/30 13:19	
11	BRS	36	pcb and ((716/1).CCLS.)	USPAT; US-PGPUB	2003/06/30 12:51	
12	IS&R	586	(716/1).CCLS.	USPAT; US-PGPUB	2003/06/30 12:51	
13	BRS	0	burnley-richard.in.	USPAT; US-PGPUB	2003/06/30 14:08	

Also see 6389558 ?
6096091 ?

→ embedded (structure or device) →

- path timing
- delays ((clock - to -output)
          or (interconnect))
- clock period

(layout same schematic) and
(Verilog same design) and
(resistance or capacitance) same
extract